

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|-----------------|------------------|---------|------------------|
| L1 | 3 | ((integrated adj circuit) IC) AND (crystalline adj silicon adj substrate) AND (conductivity near type) AND operat\$3 AND (temperature near range) AND (ion near implant\$3 near resistor) AND ((PN P/N) near junction) AND (minimum near resistance).CLM. | US-PGPUB; USPAT | OR | ON | 2005/11/22 15:55 |